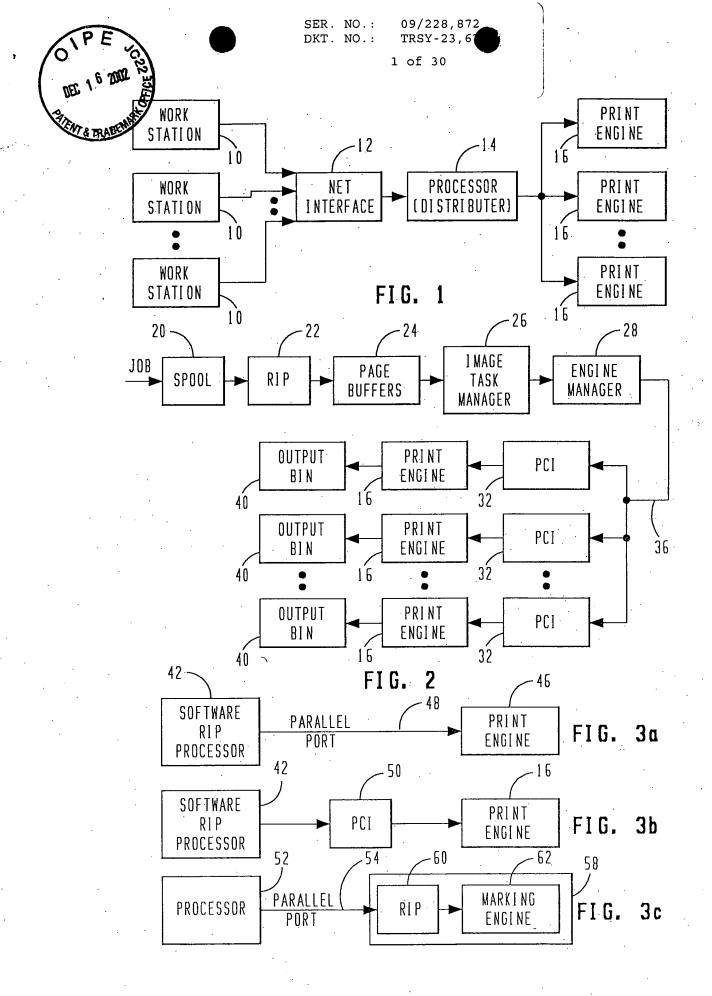


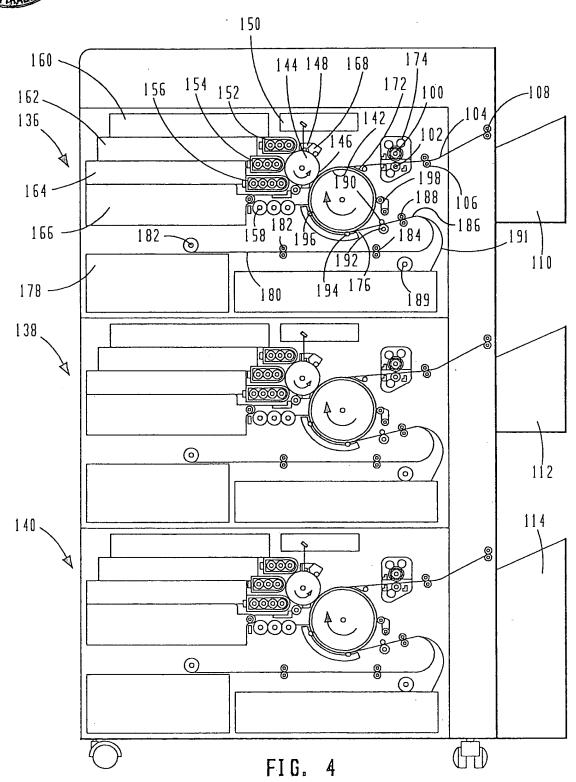
22 of 28



OTPE ICE DE LA PROPERTIE DE LA

SER. NO.: 09/228,872 DKT. NO.: TRSY-23,677

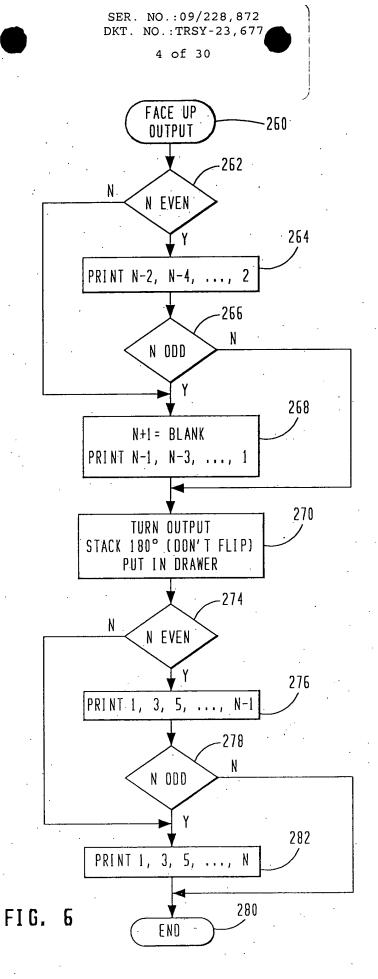
2 of 30



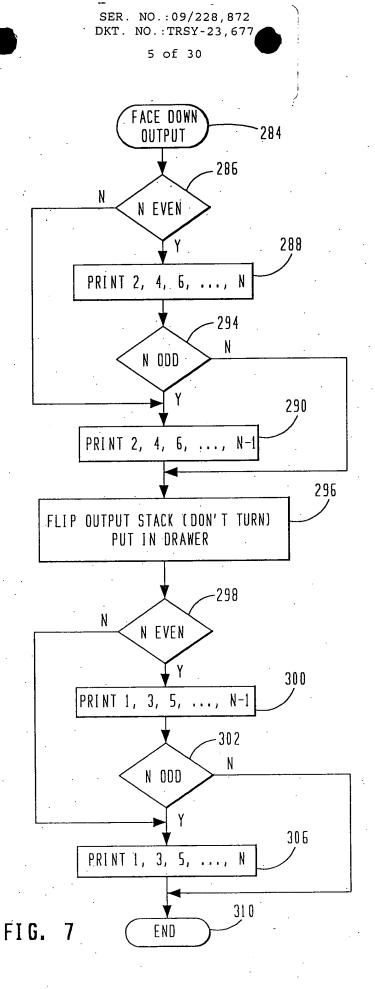
SER. NO.:09/228,872 DKT. NO.:TRSY-23,677 3 of 30 DEC 1 6 2002 STATE TRACE 230 START 234 232 USE N SINGLE ENGINE NxM ≥ E 236 N :M ≥ E 238 USE M ENGINES 244 $N \geq E$ ONLY WITH COPY "i" IN ENGINE "i" DISTRIBUTE Ε₂ E_1 $E_3 \mid E_4$ DISTRIBUTE 242 $P = \frac{N \times M}{E}$ -250 246 256 $P_1 = Nx \frac{M}{E}$ $b^3 = Nx - \frac{E}{W}$ $P_4 = NxM - (P_1 + P_2 + P_3)$ 248 $P_2 = Nx - \frac{M}{E}$

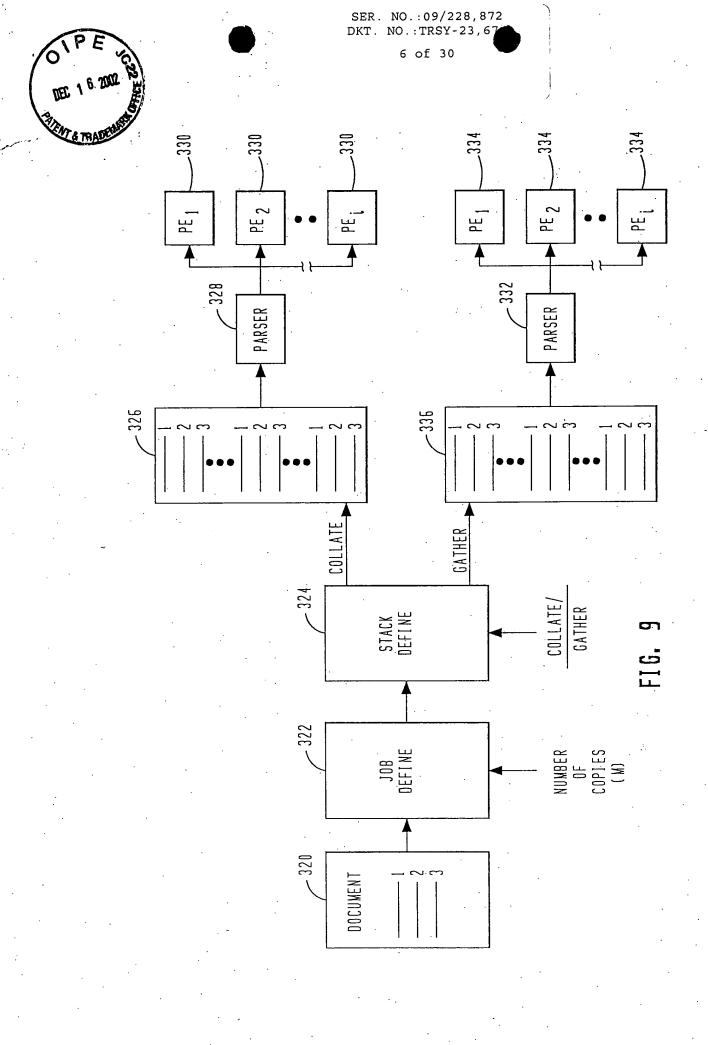
FIG. 5

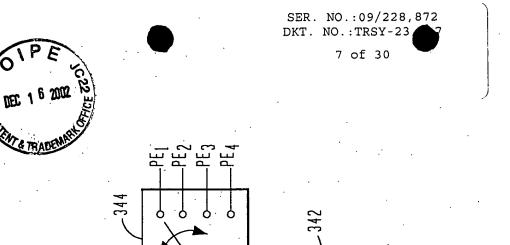


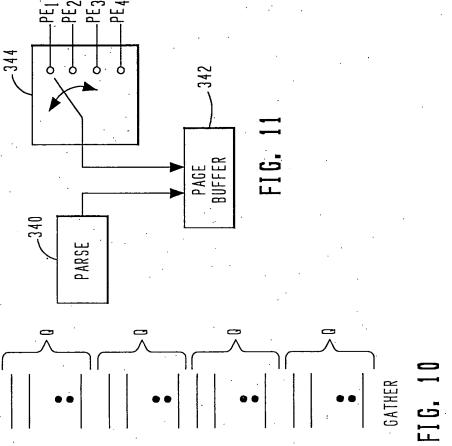


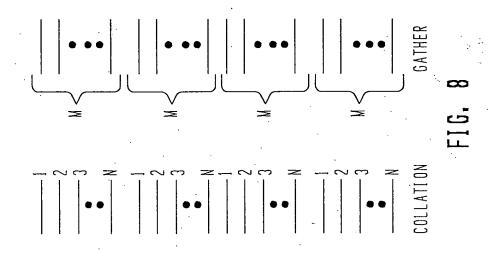












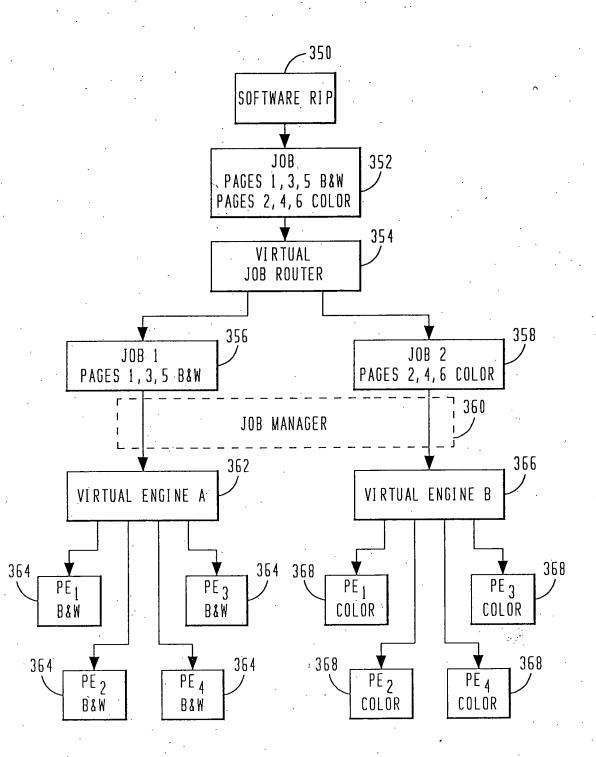
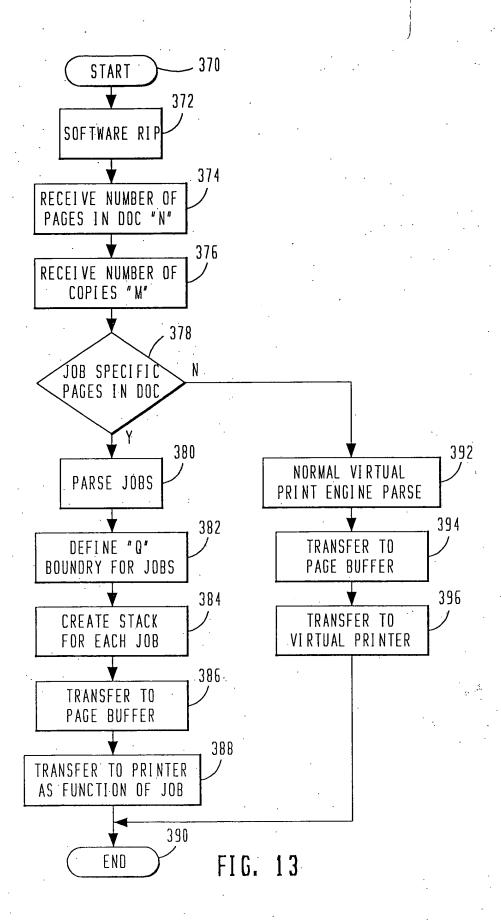
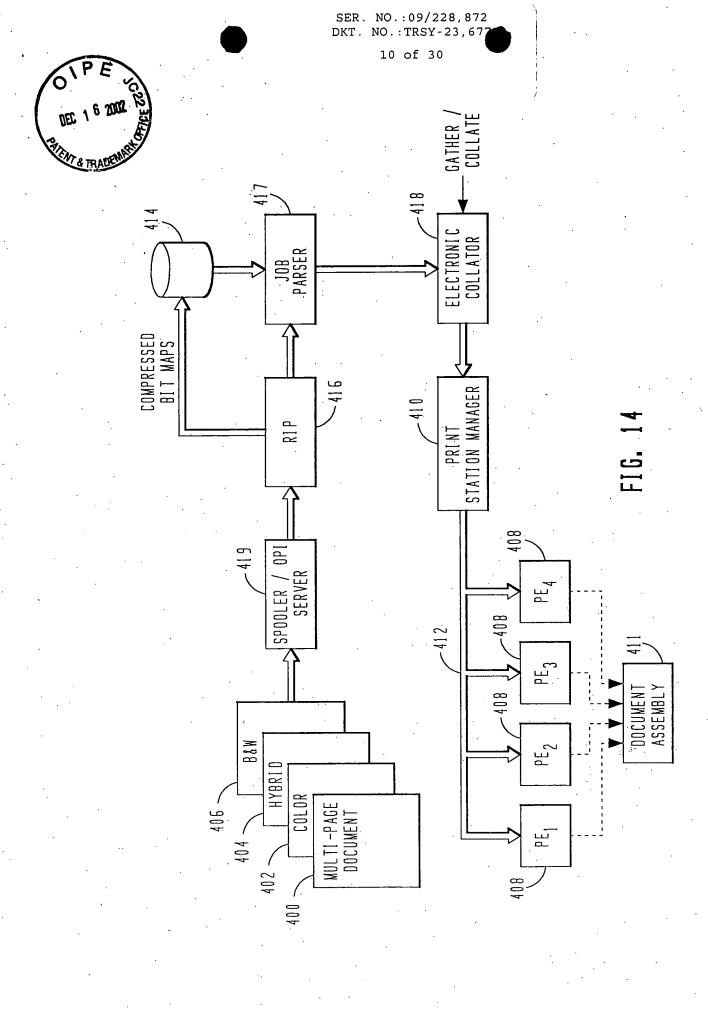


FIG. 12





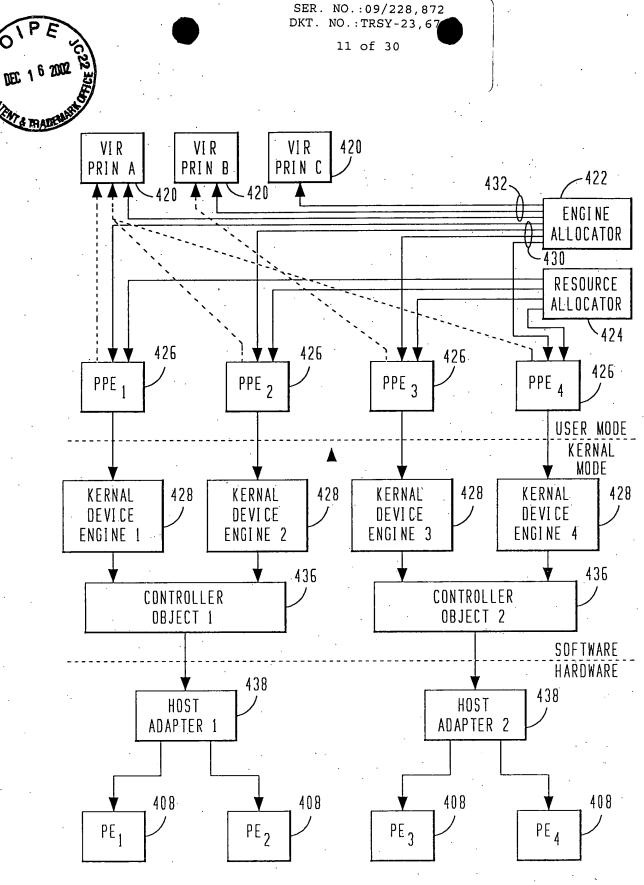


FIG. 15



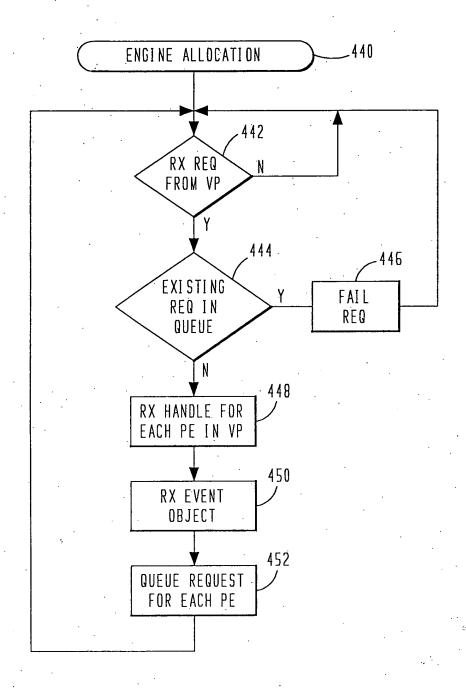
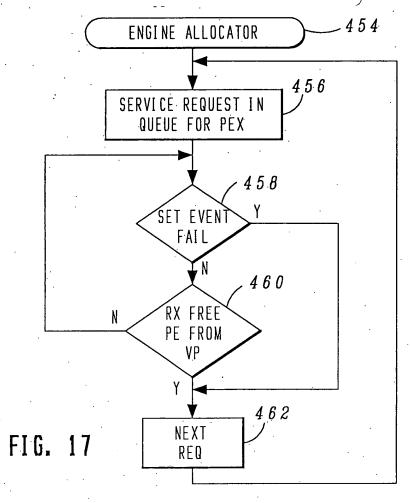
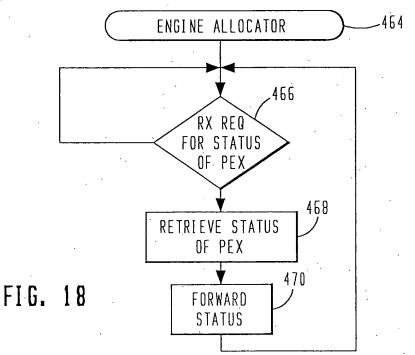
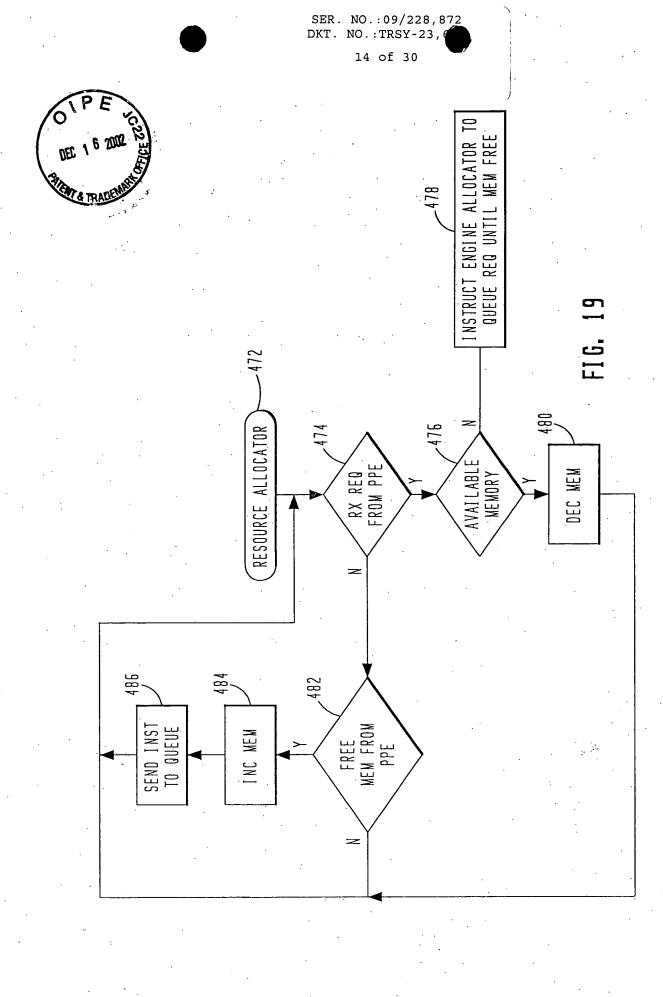


FIG. 16



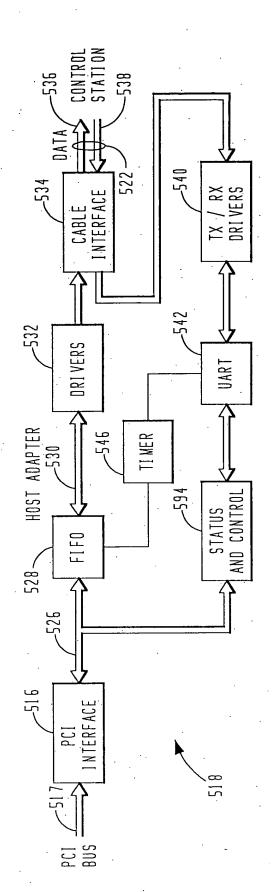




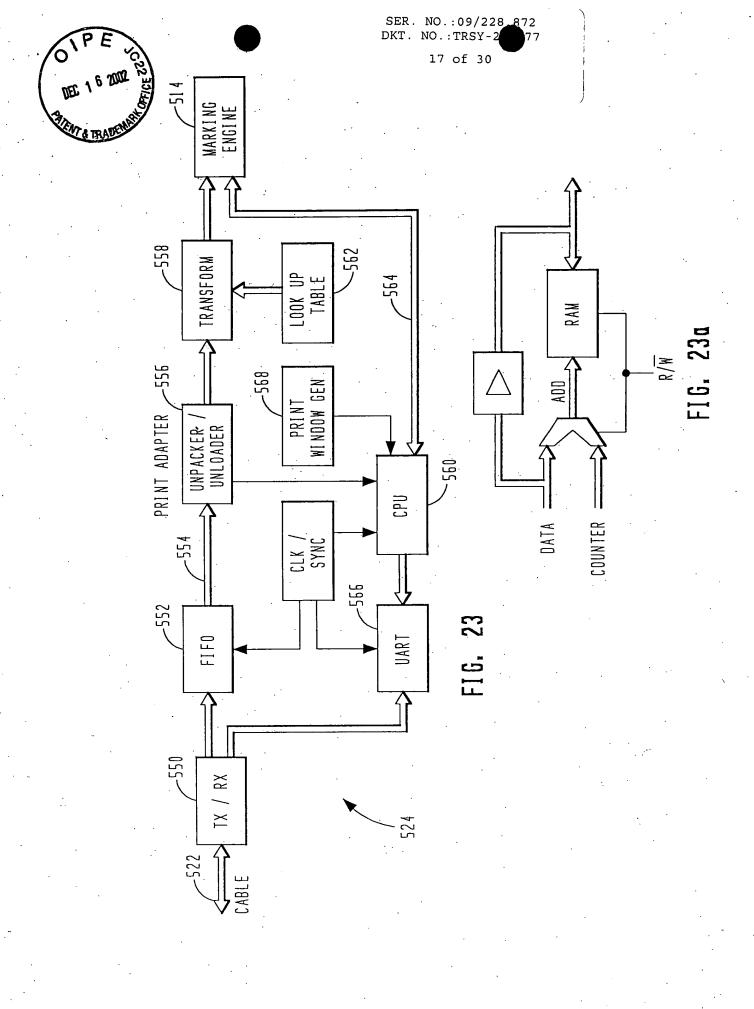


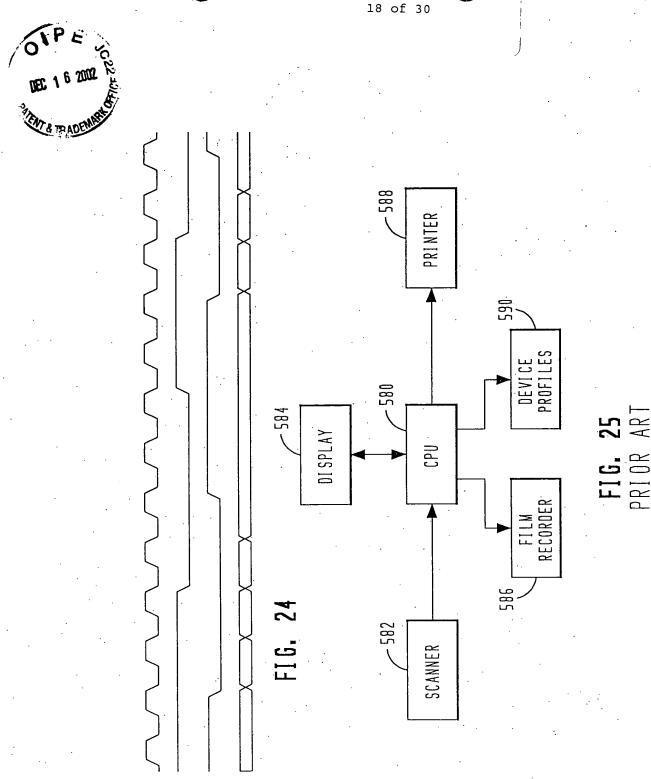
SER. NO.:09/228,872 DKT. NO.:TRSY-200677 15 of 30 -512 MEM PE 띪 -514 MARKI NG ENGI NE PRI NTER RI P -506 -508 PRINT ADAPTER PRI NT Adapter PARALLEL CABLE -502 -518 518 500 HOST ADAPTER HOST ADAPTER FIG. PRI OR 496 PCI BUS PCI Interface PCI 492 - 490 PCI BUS CPU





FI 6, 22





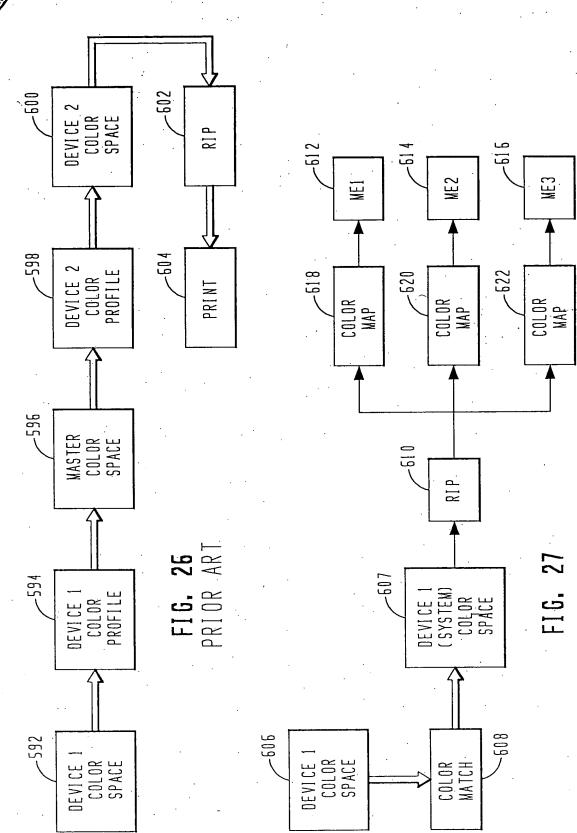
PWE×#

F x D 7: 0

PAF×#

PLCK







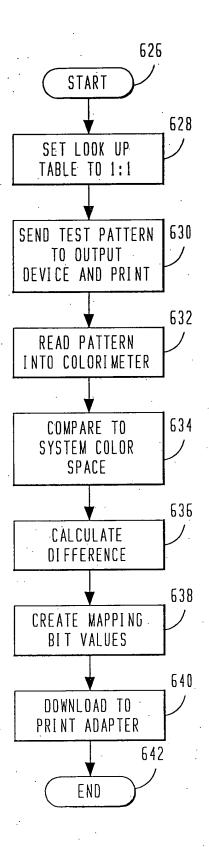
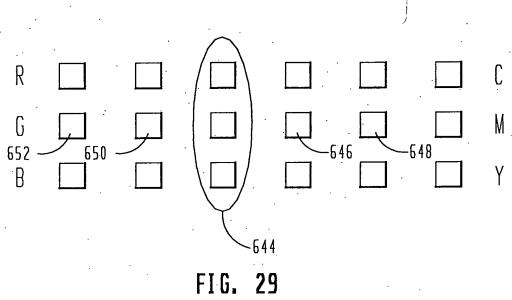


FIG. 28



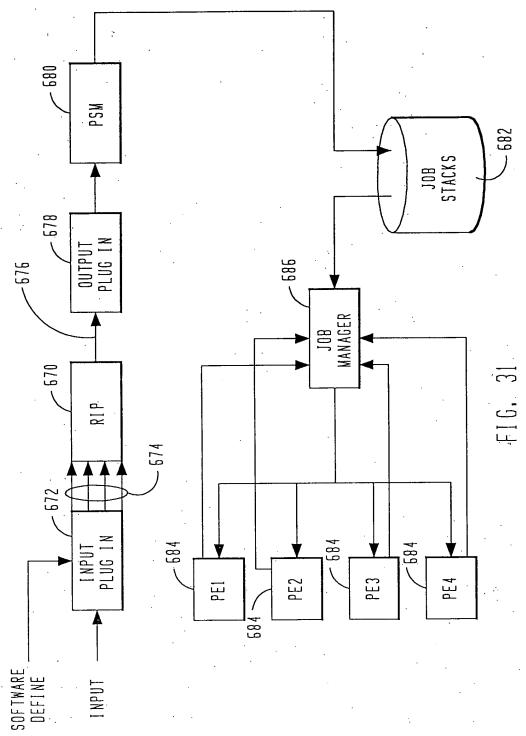


21 of 30

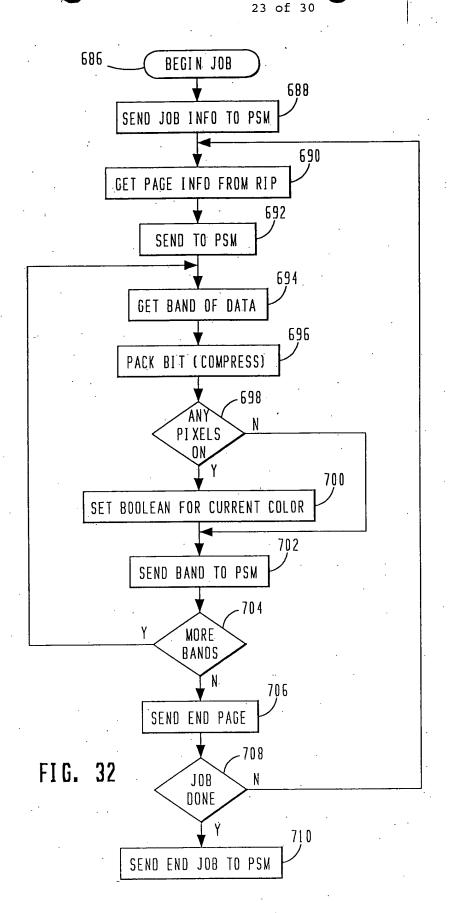
654 START 656 CREATE PATTERN BI-LEVEL 658 EXAMINE FOR VISUAL GREY 660 DETERMINE CORRECTION FOR EACH COLOR AND MAX DENSITY 662 STORE OFFSET FOR MAX DENSITY FOR BI-LEVEL 664 END

FIG. 30

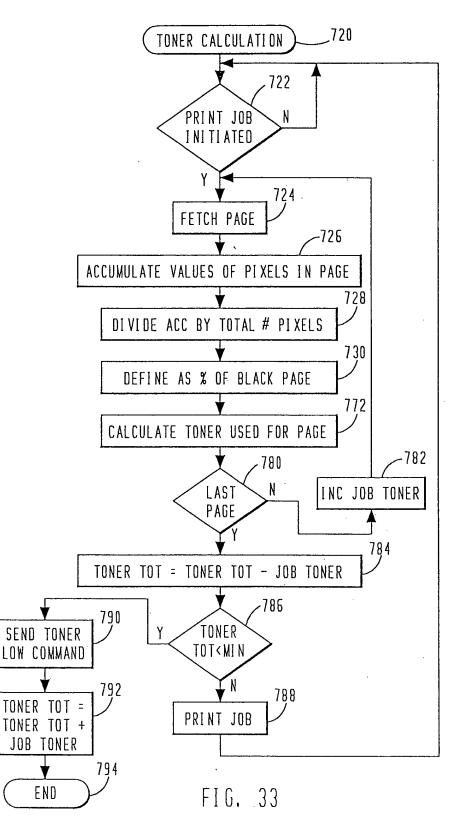














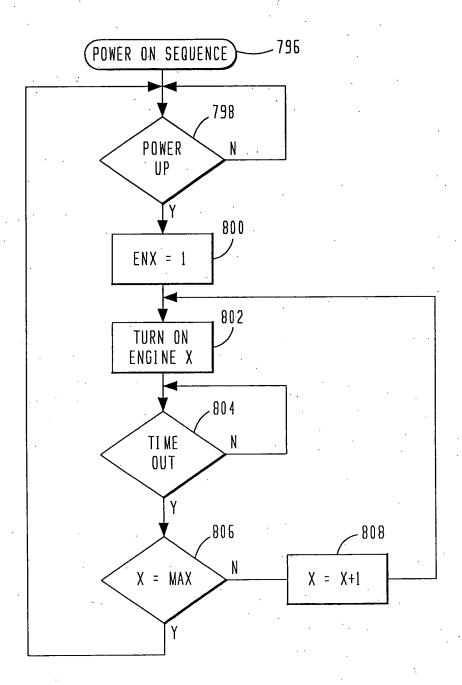


FIG. 34



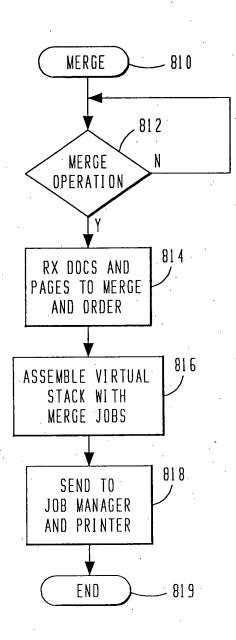


FIG. 35

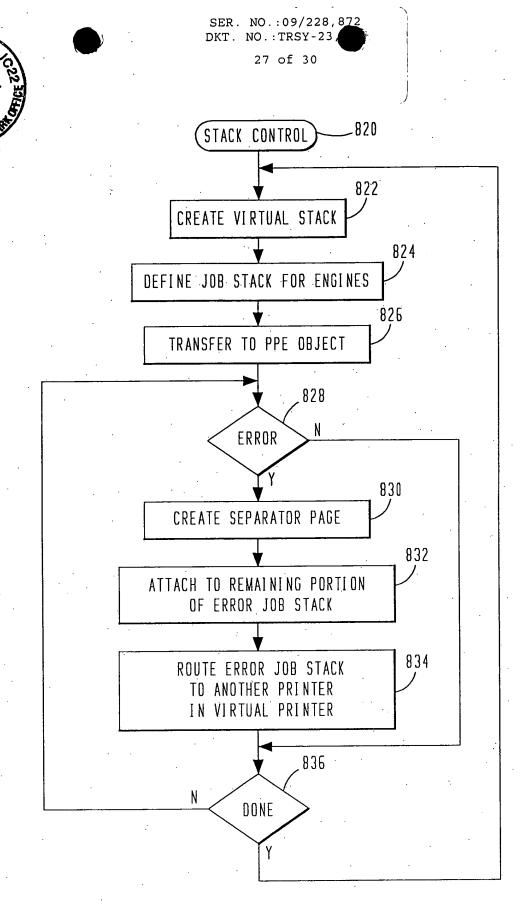


FIG. 36



